UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 7,041,976 B1 APPLICATION NO.: 10/700144

: May 9, 2006

DATED INVENTOR(S) : Neil et al.

> It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page: Item [75], under inventors, delete

-- Gabor D. Toth, San Jose, CA (US); Varoujan Chakarian, Fremont, CA (US); Douglas K. Masnaghetti, San Jose, CA (US) --

on column 9, line 2, after "stage bias voltage", delete -- is decreased --

on column 10, line 3, after "stage bias voltage", delete -- is --

Signed and Sealed this

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Fifteenth Day of August, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office